1/4

150 → **STORAGE STORAGE** <u>164</u> <u> 162</u> **INPUT STORAGE INPUT** <u>166</u> <u>170</u> **PROCESSOR** <u> 160</u> 176 OUTPUT <u> 168</u> 174

FIG. 1 (PRIOR ART)

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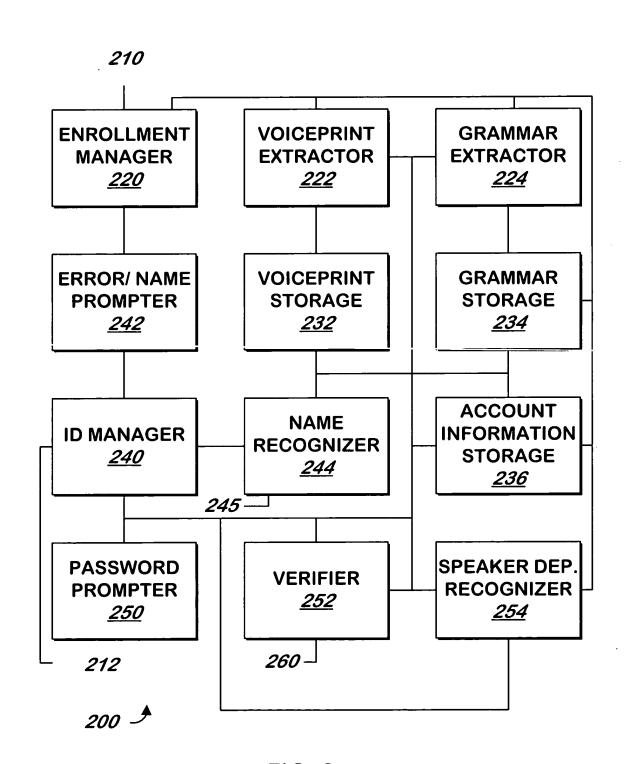
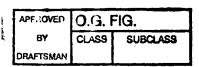


FIG. 2



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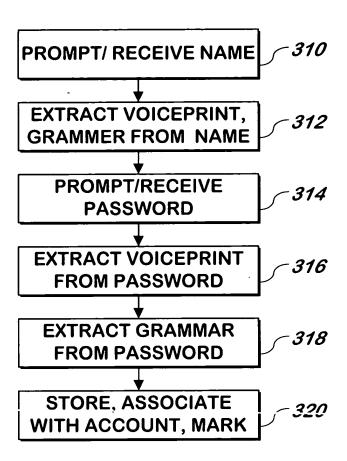


FIG. 3A





4/4

